

Electronic Acknowledgement Receipt

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Application Number:	10501461
International Application Number:	
Confirmation Number:	7547
Title of Invention:	System and method of clocking an ip core during a debugging operation
First Named Inventor/Applicant Name:	Greg Bensinger
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Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part /.zip	Pages (if appl.)
1	Foreign Reference	EP0685793.PDF	4825058 99250dedf1f623b94629579305ddc6100 4x3	no	104

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2	Foreign Reference	JP11259329.PDF	713815 0bf15b30b1aaebd8948940c32927faa8577 dabf	no	10
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3	Foreign Reference	WO0120784.PDF	1167426 4b3b41c9d596d30aa2b1be1ac2c97d05e3 81285	no	28
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4	NPL Documents	PCT_EP02_00503.PDF	352275 5bb6716cf08fd3a65672ad51ebc9fb50f022 5b38	no	7
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5	Information Disclosure Statement (IDS) Filed (SB/08)	IDS.PDF	828416 60cc1dc698fd13386f23964bbb55a7fbca 9cf7	no	4
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6	NPL Documents	Fast_hardware_software_coverage_by_optimistic_executing_of_real_processor.PDF	391252 05b798809061c7ea22ad0105d6f14c5528 a013e	no	6
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7	NPL Documents	Co_emulation_and_debugging_of_HW_SW.PDF	356299 62792fb71b4ae6ac0d2788ffce51e7ef22 d885	no	6
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8	Fee Worksheet (PTO-875)	fee-info.pdf	30246 95c73025ec625c1995bd2e49d60303568cf 2768	no	2
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